

Analog, Mixed-Signal, RF

Product Line Overview

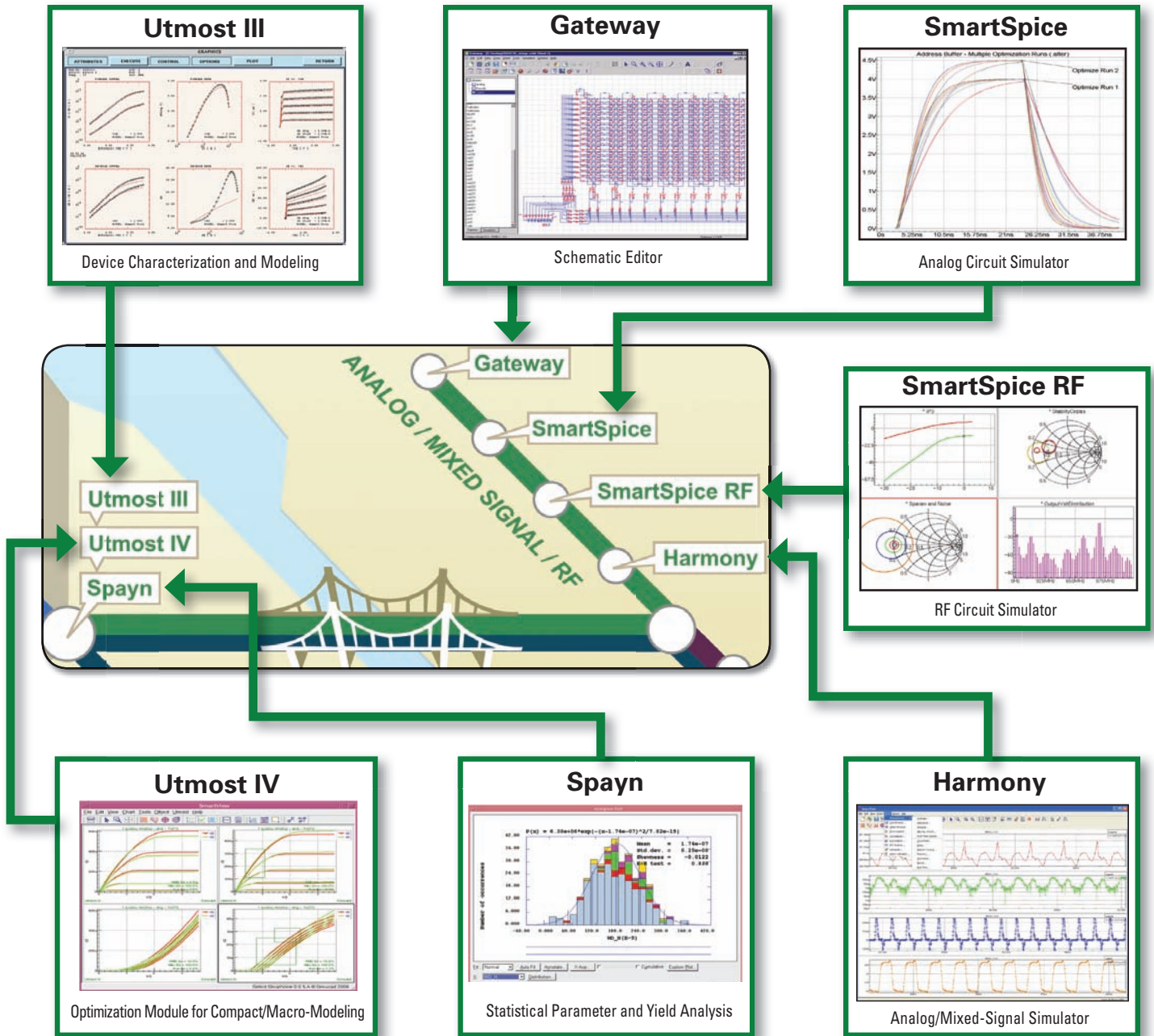


Complete schematic-driven analog, mixed-signal, and RF circuit design environment

- **Gateway - Schematic Editor and Schematic Viewer.** Supports flat or hierarchical designs of any technology. Gateway readily accepts legacy designs from other schematic editors through EDIF 2.0.0 standard.
- **SmartSpice - Analog Circuit Simulator.** Delivers the highest performance and accuracy required to design complex high precision analog circuits, analog mixed-signal circuits, analyze critical nets, characterize cell libraries, etc.
- **Verilog-A Language - Source, Compiled and Encrypted.** Combines with SmartSpice to enable the design and verification of complex analog and mixed-signal circuits and models.
- **SmartSpice RF - Harmonic Balance Based RF Simulator.** Employs Time-Domain Shooting and Frequency-Domain Harmonic Balance methods to provide simulation of GHz range RF ICs.
- **Harmony - Analog/Mixed-Signal Simulator.** Dynamically links in the capabilities of the SmartSpice Circuit Simulator and the SILOS Verilog Simulator at run time.
- **Utmost III - SPICE Modeling Software.** Generates accurate, high quality SPICE models for analog, mixed-signal and RF applications.
- **Utmost IV - Device Characterization and SPICE Modeling Building** on many years experience, Utmost IV provides a powerful yet easy-to-use tool for the characterization of devices and the generation of accurate, compact models, macro-models and Verilog-A models for analog and RF applications.
- **Spayn - Statistical Parameter and Yield Analysis.** Statistical modeling tool for analyzing variances from model parameter extraction sequences, electrical test routines, and circuit test measurements.

SILVACO

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