

## ECDSA-based Secure JTAG

### Overview

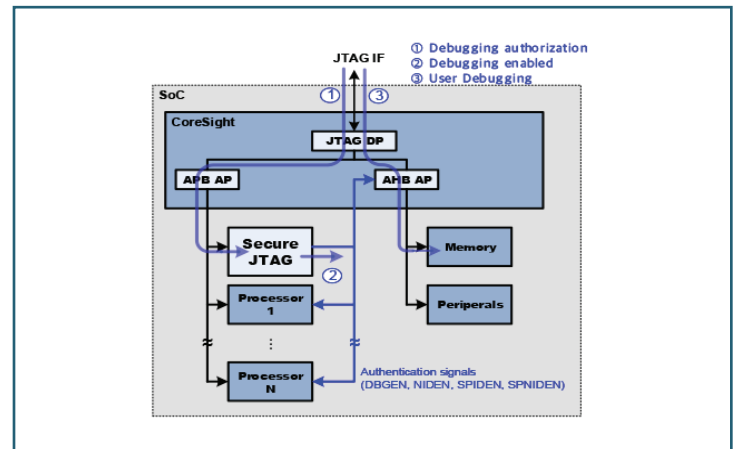
ECDSA-based Secure JTAG protects chips against malicious access through the JTAG port. Only authorized users can debug chips. We offer ready-to-use hardware implementations of the Secure JTAG.

### Key Features

- HW Turnkey Access Controller
- Secure Cryptographic Authentication
  - ECDSA P521 Sign Verification
  - Random-Challenge for Preventing Replay Attack
- Enough Granularity
  - Up to 32 Core/Blocks Separated Access Control
- E-fuse/OTP based Life-Cycle Management
  - Debug Enable → Secure Debug Enable → Permanently Debug Disabled
- 400 MHz maximum frequency (typical)

### Deliverables

- Encrypted RTL
- Integration Guide Document
- User Guide Document & T32 Debugger Script Example
- Customer-side Server Implementation Guide Document



### Target Applications

- Secure Debugging
- User/Device Authentication

# SAMSUNG Foundry

For more information, please contact us at [IP@silvaco.com](mailto:IP@silvaco.com).

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