

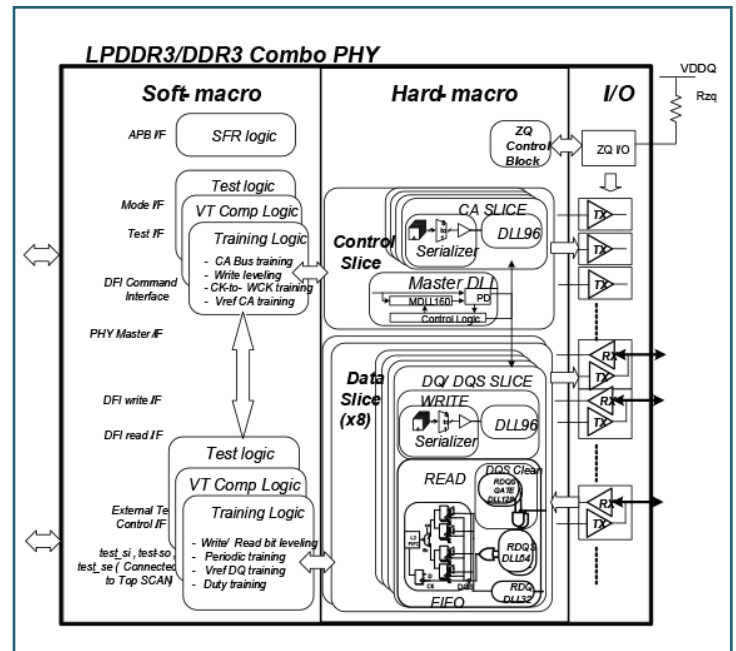
LPDDR3/DDR3 PHY

OVERVIEW

This PHY can support 1.6Gbps LPDDR3 with 32-bit 1-channel and 1.6Gbps DDR3 with 32/64-bit 1-channel configurations. It is DFI3.0 compliant with a frequency ratio of 1:2.

KEY FEATURES

- Samsung Foundry 14nm low power CMOS device technology
- Supports 1.6Gbps LPDDR3
- Supports 1.6Gbps DDR3
- Off-chip driver (OCD)
- Support Retention IO
- On-die term (ODT)
- Auto training after Memory initialization
 - o Gate Training
 - o Write/Read bit Training
- Power supply
 - o 1.0V PHY Core Voltage (typical)



DELIVERABLES

- Model and softmacro netlist
- Timing LIB
- DFT Model and netlist
- CIR, GDS2 and LEF.
- Technical Datasheet

For more information, please contact us at IP@silvaco.com.

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