

**PDK summary**

**PDK ID:** TSMC-CV013NI\_CVSP006  
**PDK version:** 0.1.3.R  
**PDK release date:** 04/07/2017  
**Fab/Foundry name:** TSMC  
**Technology name:** CV013NI (TSMC 0.13um CMOS HIGH VOLTAGE  
MIXED SIGNAL BASED LOW POWER BCD  
TRIPLE GATE SALICIDE FSG CU  
1.5/3.3/5/10/20/28/36/VG1.5/3.3/5V PDK)

**Certified Silvaco EDA tool versions for this PDK version:**

Gateway: 2.12.36.R  
SmartSpice: 4.26.7.R  
SmartView: 2.34.0.R  
Expert: 4.14.9.C \*  
Guardian: 4.10.6.R  
Hipex: 3.5.33.C \*

*\* Tool Versions modified from previous release.*