

PDK Summary

PDK ID: tsmc-025bcd40v
PDK version: 1.1.0.R
PDK file name:
PDK release date: 4/25/08
Fab/Foundry name: TSMC
Technology name: TSMC 0.25 UM CMOS HIGH VOLTAGE MIXED
 SIGNAL BASED LDMOS 1P5M SALICIDE
 2.5/5/12/40V

(1) Required Simucad EDA tool versions for this PDK version:

Gateway: 2.6.7.R
 SmartSpice: 3.8.2.R
 SmartView: 2.20.6.R
 Expert: 4.4.15.R
 Guardian: 4.4.15.R
 HIPEX: 3.2.7.R

(2) Important procedures for PDK version migration:

N/A – Initial full PDK release

(3) Changes for this PDK version (see below list for change code legends) :

N/A – Initial full PDK release

Change Codes

Affected Tool Functions:

Code A: Gateway - Schematic Edit
 Code B: Gateway - Spice Net Generation
 Code C: Gateway - LVS Net Generation
 Code D: Gateway - NDL Net Generation
 Code E: Gateway/SmartSpice - Spice Simulation
 Code F: Gateway/SmartView - Wave View
 Code G: Expert - Layout Edit
 Code H: Expert - Pcells
 Code I: Expert - NDL
 Code J: Expert/Guardian DRC - DRC
 Code K: Expert/Guardian NET - Net extraction
 Code L: Expert/Guardian LVS - LVS
 Code M: Expert/Hipex RC - RC extraction
 Code Z: Others

Change Reasons:

- Code 1: Bug fix
- Code 2: Fab/Foundry file update
- Code 3: Function improvement
- Code 9: Others