

PDK summary

PDK ID: jazz-CA18hr
PDK version: 1.1.7.R
PDK release date: 01/09/2009
Fab/Foundry name: JAZZ
Technology name: CA18HR (CMOS 0.18u 6M)

(1) Certified Simucad EDA tool versions for this PDK version:

Gateway: 2.6.7.R
 SmartSpice: 3.8.4.R
 SmartView: 2.20.6.R
 Expert: 4.4.16.R
 Guardian LVS: 4.4.15.R
 HIPEX: 3.2.7.R

(2) Important procedures for PDK version migration:

1. **EXPERT - LOADING EXISTING PROJECT (.eld file) with NEW PCELL LIBRARY (jazz_ca18hr_pcells.eld file):** Please use the new jazz_ca18hr_pcells.eld and jazz_ca18hr_esdlib.eld as pcells and standard cell library.
2. **EXPERT - LOADING CORE RULES DRC DECK (.dsf file) with NEW DRC RULE DECK (jazz-ca18hr_core_drc_rules.dsf file):** Please use the new jazz-ca18hr_core_drc_rules.dsf file.

(3) Changes for this PDK version (see below list for change code legends) :

- Modified pcells and for layer “field” existence in pcells. (Code: H2/J2) – same as 1.1.6.R update
- Modified LVS extraction rule deck for inductors (Code: L1) – same as 1.1.6.R update
- Modified jazz-ca18hr_core_drc_rules.dsf: Removed rule 3.bad. (J1)

Change Codes

Affected Tool Functions:

Code A: Gateway - Schematic Edit
 Code B: Gateway - Spice Net Generation
 Code C: Gateway - LVS Net Generation
 Code D: Gateway - NDL Net Generation
 Code E: Gateway/SmartSpice - Spice Simulation
 Code F: Gateway/SmartView - Wave View

Code G: Expert - Layout Edit
Code H: Expert - Pcells
Code I: Expert - NDL
Code J: Expert/Guardian DRC - DRC
Code K: Expert/Guardian NET - Net extraction
Code L: Expert/Guardian LVS - LVS
Code M: Expert/Hipex RC - RC extraction
Code Z: Others

Change Reasons:

Code 1: Bug fix
Code 2: Fab/Foundry file update
Code 3: Function improvement
Code 9: Others